1. A timer for controlling an appliance, comprising:

a rotatable cam-carrying member having cam surfaces thereon,

a timing motor having a rotor that rotates in response to electrical stimulation,

a drive mechanism for causing rotation of said cam-carrying member in response to rotation of said rotor,

a plurality of cam-actuated switches, each cam-actuated switch comprising first and second arms, a plurality of said arms being mounted in a first wafer and a plurality of said arms being mounted in a second wafer,

a switch wafer mounting comprising first and second locating features for receiving said first and second switch arm wafers, said first switch arm wafer resting against the first locating feature, said second switch arm wafer being stacked atop the first switch arm wafer and resting against the second locating feature.

2. The timer of claim 1 further comprising a third wafer having a plurality of said arms, wherein said switch wafer mounting comprises a third locating feature for receiving said third switch arm wafer, said third switch arm wafer being stacked atop the second switch arm wafer and resting against said third locating feature.

3. The timer of claim 1 further comprising a housing, wherein said switch wafer mounting is incorporated into said housing.

4. The timer of claim 1 wherein said first and second locating features comprise first and second steps, and the first and second switch arm wafers are sized such that the first switch wafer fits to the first step and inside of the second step, and the second switch arm wafer fits to the second step and overlaps the first.

5. The timer of claim 1 wherein said first and second locating features comprise one or more posts, each post having a first section with a first larger diameter and a second section with a second smaller diameter, and the first switch wafer defines a locating hole with a diameter larger than the first diameter, and the second switch wafer defines a locating hole with a diameter smaller than the first diameter but larger than the second diameter, such that the first switch wafer fits over the first section of each post whereas the second switch wafer fits over the second section of each post.

6. The timer of claim 1 wherein said first and second locating features comprise areas of a ramp included in said mounting, and wherein the first switch wafer is sized to intersect the ramp in a first locating area corresponding to said first locating feature, and the second switch wafer is sized to intersect the ramp in a second locating area corresponding to said second locating feature.

7. The timer of claim 1 wherein said first and second locating features comprise areas of a tapering post, and wherein the first switch wafer defines a locating hole with a diameter sized to engage the tapering post in a first locating

area corresponding to said first locating feature, and the second switch wafer defines a locating hole with a diameter sized to engage the tapering post in a second locating area corresponding to said second locating feature.